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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/706,162	11/12/2003	Farid Nemati	C-028	7584
II C Chan	7590 08/20/2007	EXAMINER		
H. C. Chan T-RAM Semio		JACKSON JR, JEROME		
620 N. McCarthy Blvd. Milpitas, CA 95035-5124			ART UNIT	PAPER NUMBER
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			MAIL DATE	DELIVERY MODE
			08/20/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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Supplemental Notice of Allowability

Application No.	Applicant(s)	
10/706,162	NEMATI ET AL.	
Examiner	Art Unit	
Jerome Jackson Jr.	2815	

Notice of Allowability	Examiner	Art Unit	
	Jerome Jackson Jr.	2815	
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not inclu will be mailed in du	ided ie course. THIS
1. This communication is responsive to <u>Board Decision</u> .		•	
2. The allowed claim(s) is/are <u>3-5,7-14,16-18,23,25 and 27</u> .			
 Acknowledgment is made of a claim for foreign priority una)	e been received. e been received in Application No cuments have been received in this r	national stage applic	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	ENT of this application.		
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			NOTICE OF
 CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the 	on's Patent Drawing Review(PTO-S s Amendment / Comment or in the O	ffice action of	he back) of
6. DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT			. Note the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal Particle 6. ☐ Interview Summary Paper No./Mail Dat 7. ☑ Examiner's Amenda 8. ☐ Examiner's Stateme 9. ☐ Other	(PTO-413), e nent/Comment	llowance
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An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

Rewrite allowed claims 23, 25 and 27 as follows:

23. A semiconductor device comprising:

a thyristor having thyristor body regions including first and second immediately adjacent base regions between first and second emitter regions;

a first control port configured and arranged to capacitively couple a first signal at least to the first base region; and

a second control port configured and arranged for receiving a second signal generated outside of the thyristor and for coupling the second signal at least to the second base region, the second signal being adapted to control holding current or forward blocking voltage of the thyristor as a function of temperature;

wherein one of the base regions includes N-doped material having a higher concentration of N+ dopant in a depletion region that faces the second control port.

25. A memory device comprising:

at least one thyristor having thyristor body regions including first and second immediately adjacent base regions respectively coupled to and between first and second emitter regions;

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a first control port configured and arranged to capacitively couple a first signal at least to the first base region;

a first circuit configured and arranged to detect a temperature-related failure of the thyristor to maintain its conductance state during a standby mode or to maintain its blocking state; and

a second circuit including a second control port configured and arranged for receiving a second signal generated outside of the thyristor and for coupling the second signal at least to the second base region as a function of the detected failure for controlling holding current or forward blocking voltage of the thyristor;

wherein one of the base regions includes N-doped material having a higher concentration of N+ dopant in a depletion region that faces the second control port.

27. A semiconductor device comprising:

a thyristor having thyristor body regions including first and second immediately adjacent base regions between first and second emitter regions, the thyristor body being maintained in a conductance state as a function of holding current; and

a control circuit configured and arranged for applying a signal to at least one of the base regions for controlling the holding current or forward blocking voltage as a function of temperature, the signal being generated outside of the thyristor;

further comprising a second control port,

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wherein one of the base regions includes N-doped material having a higher concentration of N+ dopant in a depletion region that faces the second control port.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jerome Jackson Jr. whose telephone number is 571-272-1730. The examiner can normally be reached on M-Th.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ken Parker can be reached on 571-272-2298. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

jj

JEROME JACKSON PRIMARY EXAMINER